

Attorney's Docket No. 354096.01201 - PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Lester J. Kozlowski; Markus Loose

Serial No.: Not Known

Filed: Herewith

For: CMOS IMAGING SYSTEM WITH LOW FIXED
PATTERN NOISE

Examiner: Unknown

Group Art Unit: Unknown

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents and Trademarks
Alexandria, VA 22313-1450

Sir:

Applicant submits herewith patents, publications or other information (listed on the attached Form PTO/SB/08A) of which Applicant is aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR §1.56.

In accordance with 37 CFR §1.97(h), the filing of this Information Disclosure Statement shall not be construed to be an admission that the information cited in this statement is, or is considered to be, material to patentability as defined in 37 CFR §1.56(b).

In accordance with 37 CFR §1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR §1.56(a) exists. It is submitted that the Information Disclosure Statement is in compliance with 37 CFR §1.98 and MPEP §609 and the Examiner is respectfully requested to consider the listed references.

Respectfully submitted,

REED SMITH LLP

Dated: February 11, 2004

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Attached: Form PTO-1449

FORM PTO-1449 (Rev. 7-80)		U.S. Dept. of Commerce Patent and Trademark Office		Atty Docket No. 354096.01201		Serial No.: Not Known		
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				Applicant: Lester J. Kozlowski, Markus Loose				
				Filing Date: Herewith		Group: Not Known		
U.S. PATENT DOCUMENTS								
*Examine r Initials	AA	Document Number	Date	Name	Class	Subclass	Filing Date	
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	AB	5,892,540	04/06/99	Kozlowski, et al			06/13/96	
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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)								

	AV	Youxin Gao; D.F. Wong, "Wire-sizing for Delay Minimization and Ringing Control Using Transmission Line Model" Department of Computer Sciences, University of Texas at Austin, Austin, Texas 78712
	AW	K. Chen, M. Afghahi, P.E. Danielsson, C. Svensson "PASIC: A Processor-A/D converter-Sensor Integrated Circuit"; 1990 IEEE, pages 1705-1708
	AX	Keping Chen, Per-Erik Danielsson, Anders Astrom "PASIC A Sensor/Processor Array For Computer Vision"; 1990 IEEE, pages 352-366
	AY	Boyd Fowler, Abbas El Gamal, David X.D. Yang "A CMOS Area Image Sensor with Pixel-Level A/D Conversion"; 1994 IEEE International Solid-State Conference, pages 226-227.
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Examiner		Date Considered
<p>*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw Line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>		